

บรรณานุกรม

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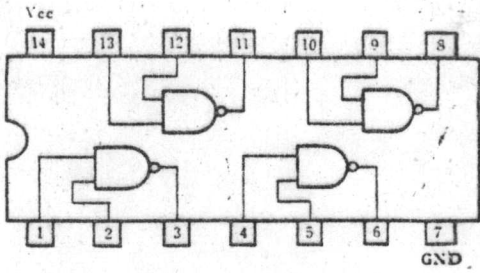
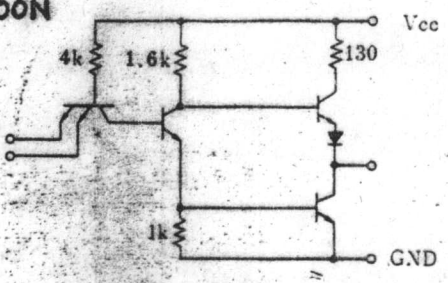
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ภาคผนวก

MONOLITHIC DIGITAL INTEGRATED CIRCUIT
TTL SERIES M53200P (SN 74N) PLASTIC MOLDED DIL PACKAGE

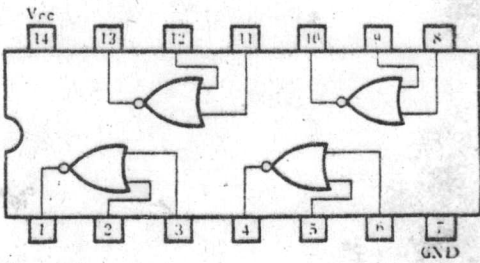
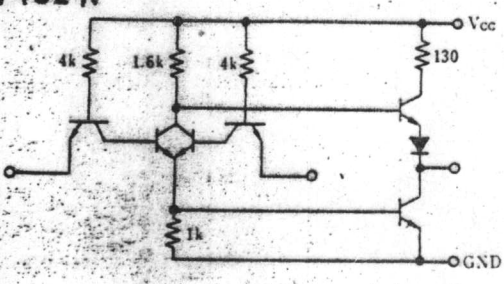


M 53200 P
SN 7400N



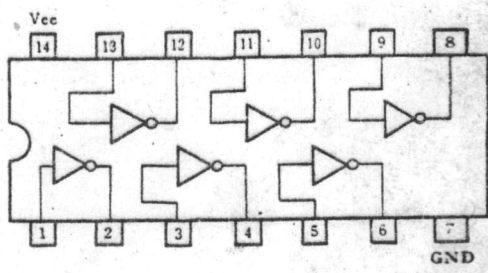
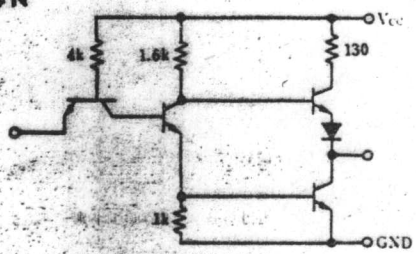
Quadruple 2-Input Positive NAND Gate

M 53202 P
SN 7402N



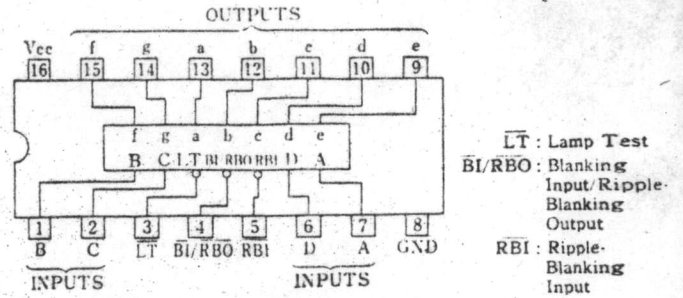
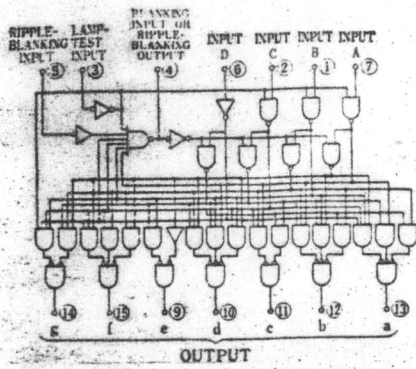
Quadruple 2-Input Positive NOR Gate

M 53204 P
SN 7404N



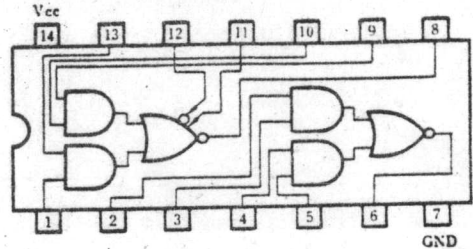
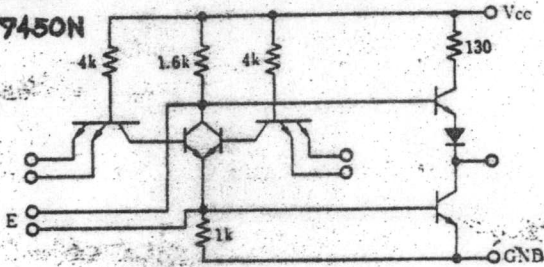
Hex Inverter

M 53248 P
SN 7448N



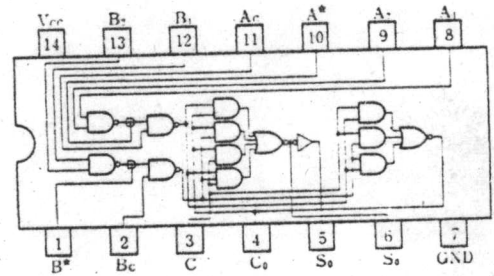
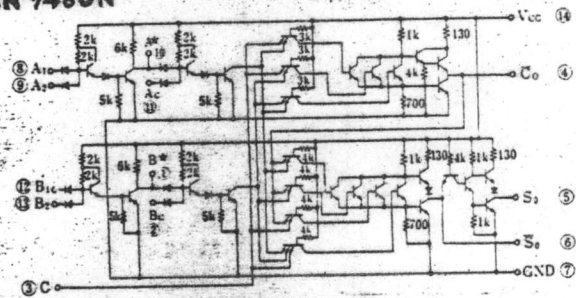
BCD-to-Seven-Segment Decoder/Driver
(Active High Outputs)

M 53250 P
SN 7450N



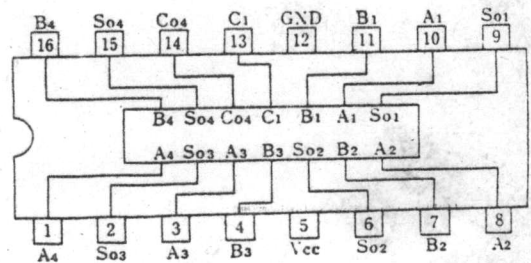
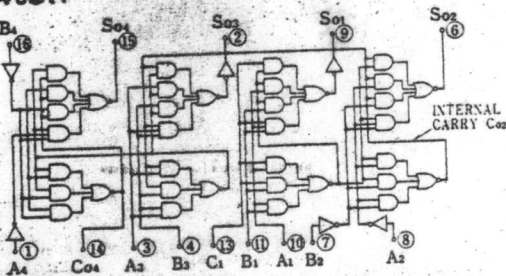
Expandable Dual 2-Wide 2-Input AND-OR-INVERT Gate

M 53280 P
SN 7480N



Gated Full Adder

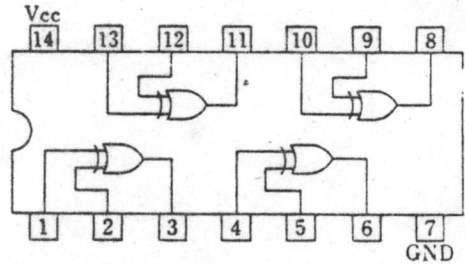
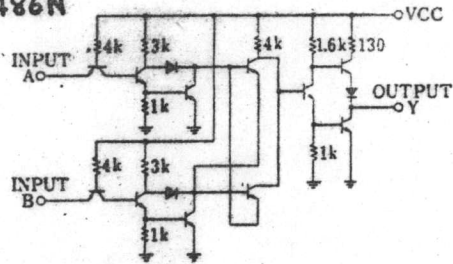
M 53283 P
SN 7483N



4-Bit Binary Full Adder

M 53286 P

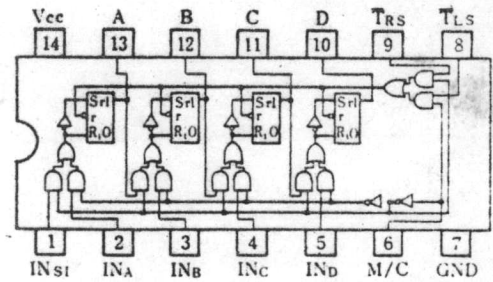
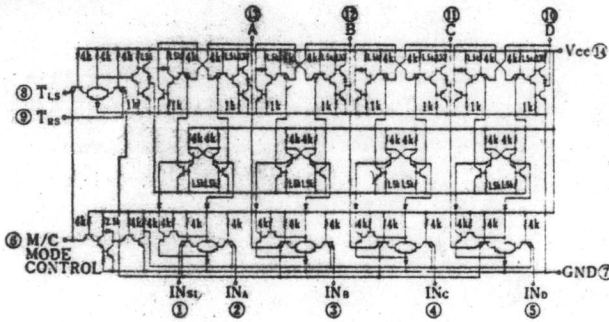
SN 7486N



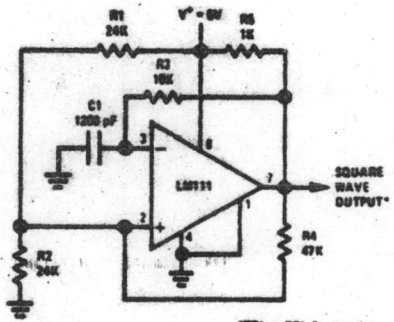
Quadruple 2-Input Exclusive-OR Gate

M 53295 P

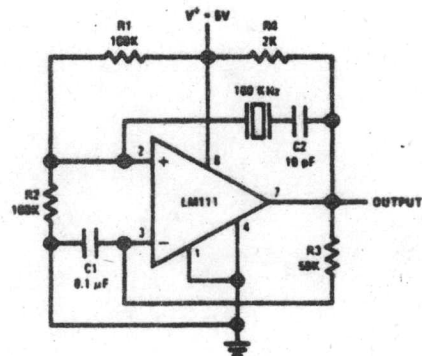
SN 7485N



4-Bit Right-Shift Left-Shift Register



FREE-RUNNING MULTIVIBRATOR



CRYSTAL-CONTROLLED OSCILLATOR

ประวัติการศึกษา

ผู้เขียนวิทยานิพนธ์

นายประสาธ อรามกุล

วุฒิการศึกษา

ปริญญาวิทยาศาสตรบัณฑิต จุฬาลงกรณ์-
มหาวิทยาลัย ปี พ.ศ. 2508

ตำแหน่งปัจจุบัน

อาจารย์โทวิทยาลัยเทคนิคภาคใต้ สงขลา

